

27. The invention as defined in claim 26 wherein the data file is stored on a CD-Rom.

**REMARKS**

The Office action dated January 25, 2002 and the references cited therein have been carefully considered. This Action is not made Final.

Applicant's representative respectfully posits that the PTO-90C (Rev. 07-01) provided with the First Office Action incorrectly cites Art Unit 3744 and Examiner Denise Lynne Esquivel as of record on this case, Serial No. 09/345,659. Applicant's representative has prepared this Office Action Response directed as in the body of the text to Examiner Michael N. Opsasnick in Art Unit 2654. If any additional actions on the part of the Applicant or the Applicant's representative are required to correct this error, the Applicant looks forward to direction from Examiner Opsasnick.

Claims 1 through 27 are pending in the application. All claims stand rejected by this First Office Action, no claims stand objected to or allowed.

Claim 4 has been canceled without prejudice.

In view of the following remarks, all pending claims overcome the rejections set forth in the Office Action and are in condition for allowance. Accordingly, favorable reconsideration of all pending claims is respectfully requested.

**Claims Rejection – 35 USC Section 112**

Claims 4 and 5 stand rejected under 35 U. S. C. Section 112, second paragraph, as being indefinite for failing to particularly point out and distinctly claim the subject matter which Applicant regards as the invention.

Specifically, Claim 4 recited the limitation of “said last step” without apparent antecedent basis for this limitation in the claim. Claim 5 was deemed to be vague and indefinite, because Claim 5 referred to Claim 4, without correcting the indefiniteness found in Claim 4.

Applicant deletes Claim 4 by this Office Action Response and amends Claim 5 as dependent upon amended Claim 1.

Applicant believes this deletion and amendment now places Claim 5 in condition of allowance.

**Claims Rejection – 35 USC Section 102**

The Examiner held in his Office Action that recent changes made to 35 U. S. C. Section 102 (e), by the American Inventors Protection Act of 1999 (AIPA), do not apply to the examination of this application. He based this decision on the fact that the application being examined was not (1) filed on or after November 29, 2000, or (2) voluntarily published under 35 U. S. C. 122(b).

Applicant concurs that this present application is to be examined under 35 U. S. C 102(3) as being anticipated by the United States to Patent No. 6,226,758 B1 issued to Gaalaas et al.

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Specifically, the Examiner held that as per claims 1, 15 through 19, 2, 3, 6, 7, 8, 9 through 11, 13, 14, 12, 20, 21, 22, 23, 24, 25, 26 and 27 were all anticipated by various elements of the invention taught by Gaalaas et al.. This patent to Gaalaas et al. teaches a sample rate converter for non-audio AES data channels, wherein channel status information is block transferred, access to receive channel blocks is facilitated and where the re-transmitted blocks are sent at the output sample rate.

Applicant by this Office Action Response amends Claims 1, 6, 15, 17 and 21 more clearly describing the present invention apparatus and methods for transmitting video and motion picture broadcasts with AC-3 audio compression systems with direct from the audio studio multi-channel audio capability in a direct satellite broadcast system.

Reconsideration of this rejection in view of the amendments to Claims 1, 6, 15, 17 and 21 is respectfully requested. The Applicant posits that the United States Patent to Gaalaas et al. does not teach nor describe the present invention, a method and apparatus to accomplish the "studio direct" broadcast of video and television programming recorded with AC-3 through an AES-3 transport.

**Prior Art Cited by the Examiner Made of Record**

The Prior Art made of record and not relied upon, considered pertinent to Applicant's disclosure are the United States Patents to Mori et al. (US-

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6,208,802), Ichikawa et al. (US-5,917,836), Hinderks (US-6,332,119), Hinderks (US-6,041,295), Smyth et al. (US-5,956,674) and Non Patent Documents, the articles to Johnston, "MPGE-Audio Draft, Description as of Dec. 10, 1990", pp. 336-337, 191 IEEE and Angelici et al., "New Architecture for an AES-EBU Digital Audio Receiver", pp. 694-698, IEEE.

Applicant has considered these references enumerated above and have not found them to be of particular relevance and materiality to the present invention.

The Examiner did not object to the drawings originally filed. The originally filed drawings are not amended by this Office Action response.

Applicant by this Office Action response have amended Claims 1, 6, 15, and 21 and as per the recommendation of the Examiner. These claims and all those claims dependent thereon are now in condition for allowance

**CONCLUSION**

In conclusion, applicant respectfully submits that the amendments and remarks herein have overcome every ground for rejection. Accordingly, the application is in condition for allowance, and early notice to this effect would be greatly appreciated.


If, for any reason, the Examiner believes that a telephone conference would help clear up any unresolved matters, the Examiner is respectfully

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requested to contact the undersigned attorney at the telephone number listed below.

Respectfully submitted,

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**APPENDIX "A"**

**VERSION WITH MARKINGS TO SHOW CHANGES MADE**

Please amend the claims without prejudice as follows:

1. **(ORIGINAL CLAIM – AMENDED ONE TIME)**      The method for  
automatically measuring, monitoring, and switching [for handling the  
switching of] signals for audio broadcasts of program content in AC-3  
standard format to be transmitted as AES-3 signal bit streams, said audio  
broadcasts containing meta data, variable dependent upon said program  
content comprises:

determining a predetermined count at which each packet in the  
AES-3 bit stream is to arrive, disabling a response to receipt of the packet  
to avoid outputting the data in the packet if it is received before said  
predetermined count has lapsed from receiving the start of a packet,

detecting if a disruption occurs while packet is being received,  
and

accepting the packet of AC-3 information for enabling output  
after a predetermined time period plus the predetermined count from which  
the last packet started, if a disruption has been detected[. ], and

determining whether said last packet comes within 10 milliseconds after an AC-3 packet was predicted to have arrived, and accepting said last packet as a trigger to provide a valid output in response to said detection.

2. **(ORIGINAL CLAIM)**                      The invention as defined in claim 1 wherein said count is a time count.
3. **(ORIGINAL CLAIM)**                      The invention defined in claim 1 wherein said count is a word count.
4. **(DELETED BY THIS ACTION)**                      The invention as defined in claim 1 wherein said last step comprises determining whether the under packet comes within 10 milliseconds after an AC-3 packet was predicted to have arrived, and accepting said packet as a trigger to provide a valid output in response to said detection.
5. **(ORIGINAL CLAIM – AMENDED ONE TIME)**                      The invention as defined in claim [4] 1 and further comprising wherein if another packet comes within a time period greater than 10 milliseconds but less than the predetermined number of milliseconds between packets, and preventing

output by refusing to accept the packet, received in said interval between N and X for enabling output.

6. **(ORIGINAL CLAIM – AMENDED ONE TIME)** A method for controlling the status of channel status bits in multiple data streams, said multiple data streams operable to provide video, uncompressed stereo digital data in a first language and uncompressed stereo digital data in at least a second language comprises:

establishing agreement between the channel status bit buried in an AC-3 packet and the channel status bit buried within the MPEG-2 PES header structure, and

regenerating the channel status bits of the AES-3 stream continuing in the IRD for output of AC-3 to feed an external AC-3 decoder so that the channel status bits comply with the bits in the AC-3 data stream which also agrees with the serial data stream between the IRD and the decoder.

7. **(ORIGINAL CLAIM)** The invention as defined in claim 6 wherein said establishing agreement comprises parsing the AC-3 bit stream, determining the channel bit status, setting the channel bit status in MPEG-2 PES header, and generating MPEG-2 PES header in an encoder.



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8.     **(ORIGINAL CLAIM)**       The invention as defined in claim 7 wherein said establishing agreement comprises setting the AC-3 audio stream channel status bit to be on, and recalculating the CRC bit in response to changing the channel status bit in the AC-3 audio stream.

9.     **(ORIGINAL CLAIM)**       The invention as defined in claim 8 wherein said setting comprises an operator manually setting said channel status bit.

10. **(ORIGINAL CLAIM)**       The invention as defined in claim 8 which said setting comprises automatically setting said channel status bit.

11.   **(ORIGINAL CLAIM)**       The invention as defined in claim 8 wherein said channel status bit is a copyright status bit.

12.   **(ORIGINAL CLAIM)**       The invention as defined in claim 8 wherein said establishing agreement comprises setting the AC-3 audio stream channel bit to be off, and recalculating the CRC bit in response to changing the copyright bit status in the AC-3 audio stream.

13.   **(ORIGINAL CLAIM)**       The invention as defined in claim 12 wherein said setting comprises an operator manually setting said copyright bit.

14.   **(ORIGINAL CLAIM)**       The invention as defined in claim 12 which said setting comprises automatically setting said copyright bit.

15.    **(ORIGINAL CLAIM – AMENDED ONE TIME)**           An uplink processor for transmitting AC-3 audio streams together with video transmissions, the uplink processor incorporated with in a direct satellite broadcast system comprising:

an encoder with switch logic input automatically sensing sensor audio signal formats and redirecting signals to an encoder adapted to process said second audio signal formats.

16.    **(ORIGINAL CLAIM)**           The invention as defined in claim 15 wherein said sensing comprises the sensing of compression in a serial digital interface router.

17.    **(ORIGINAL CLAIM- AMENDED ONE TIME)**           An apparatus for automatically checking cataloging and reporting errors in an AC-3 bit stream signal broadcast in a direct satellite broadcast system comprising:

a monitor including a processor with a clock,  
a computer interface for converting AES signal to computer readable language and,  
time code labeler for labeling each packet with a time stamp with said processor controlling said computer interface to read data into a buffer, sending a message to the processor's operating system in response to a detector detecting errors in said data and sending a message in response to discrepancies with said labeling.

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18. **(ORIGINAL CLAIM)** The invention as defined in claim 17 wherein said detector comprises a state processor finding AC-3 packets, locking into each packet, and detecting any discontinuity in the packets or loss of packets from a signal.

19. **(ORIGINAL CLAIM)** The invention as defined in claim 17 wherein said detector is operative checking timing of on-air faults simultaneously with on-air broadcasting of said AC-3 bit stream signal.

20. **(ORIGINAL CLAIM)** The invention as defined in claim 19 wherein said detector is operative while preparing a tape for broadcast.

21. **(ORIGINAL CLAIM – AMENDED ONE TIME)** A device for playing AC-3 signal out in sync with a video signal, said device incorporated within a direct satellite broadcast system providing program content to a terrestrial receiver comprising :

a monitor including a processor with a time clock, wherein a sound card has an input for a) receiving a house reference AES clock and b) locking audio output to the frequency of video production of a house master generator by said audio card receiving AES clock input to simultaneously start audio and AC-3 data stream.

22. **(ORIGINAL CLAIM)** The invention as defined in claim 21 and further comprising a time code reader for automatic start of playback of said video and audio signals in sync.

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23.    **(ORIGINAL CLAIM)**       The invention as defined in claim 21 wherein an editor's contact closure is an input to said processor to start playback.

24.    **(ORIGINAL CLAIM)**       The invention as defined in claim 21 wherein said monitor determines a size of the packet from a start of the packet, and generates an IEC 958 header based on said determined size,

25.    **(ORIGINAL CLAIM)**       The invention as defined in claim 24 wherein said device places the header and AC-3 packet in buffer, supplements the unfilled spaces in the buffer with a series of bits, and wherein said device outputs data from the AES-3 card as PCM audio signal.

26.    **(ORIGINAL CLAIM)**       The invention as described in claim 21 wherein the AC-3 data is stored as a data file.

27.    **(ORIGINAL CLAIM)**       The invention as defined in claim 26 wherein the data file is stored on a CD-Rom.